

as the last,

FIG. 2A

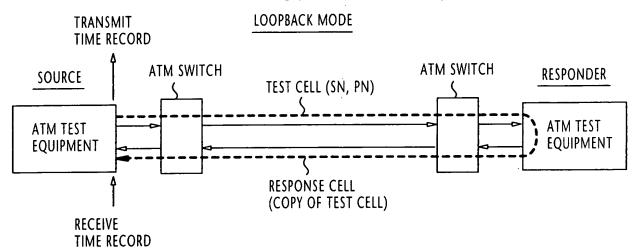
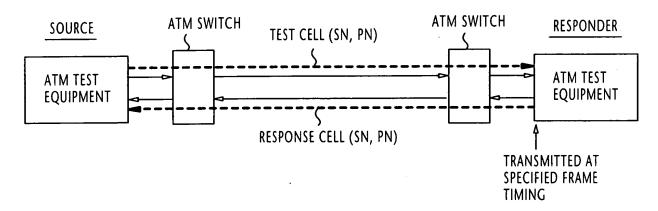


FIG. 2B

TWO-WAY MODE



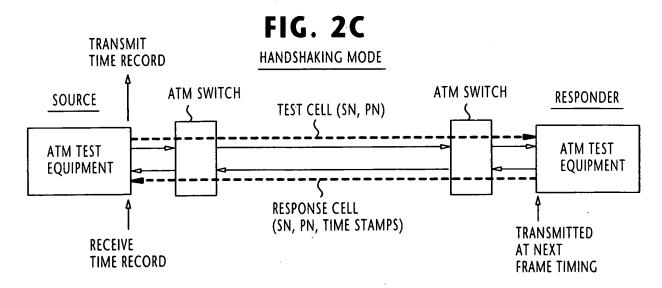


FIG. 3A

P SEQUENCE NUMBER
TEST MODE
A
Y
L
O
A
D
D
TEST MODE

FIG. 3B

SOURCE AND RESPONDER NODES (LOOPBACK MODE)

ATM CELL HEADER
SN
MODE 1
PSEUDONOISE BIT PATTERN

FIG. 3C

SOURCE AND RESPONDER NODES (TWO-WAY MODE)

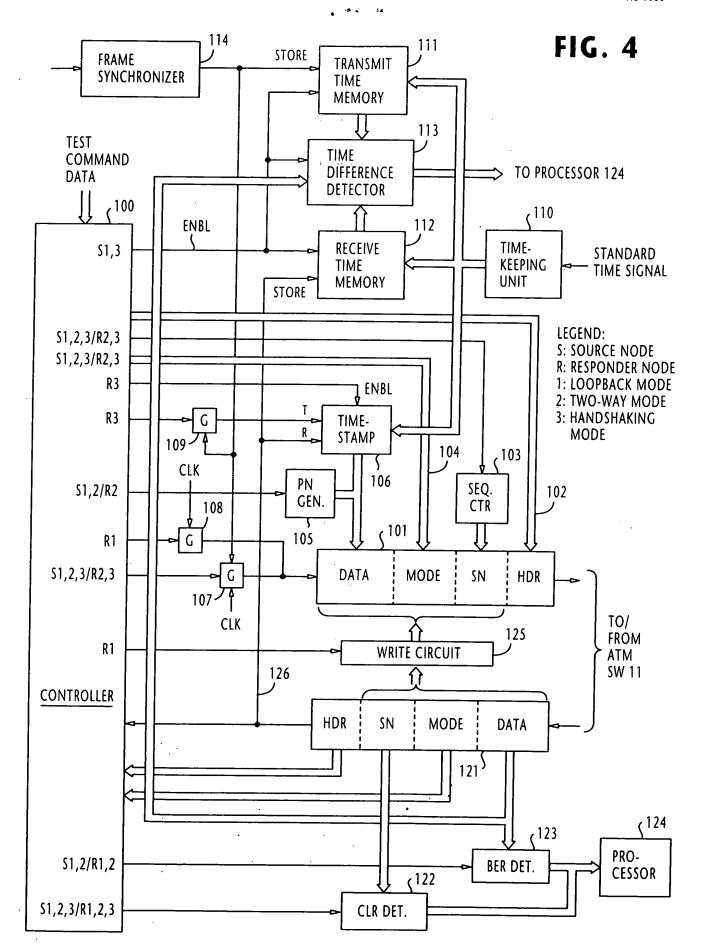
ATM CELL HEADER
SN
MODE 2
PSEUDONOISE BIT PATTERN

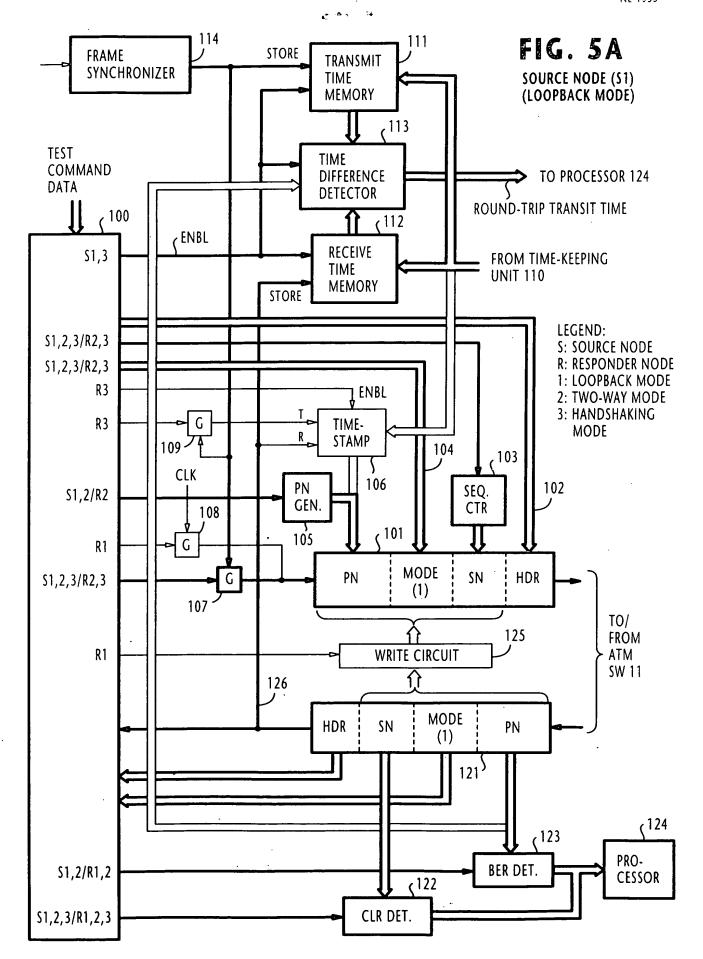
FIG. 3D
SOURCE NODE
(HANDSHAKING MODE)

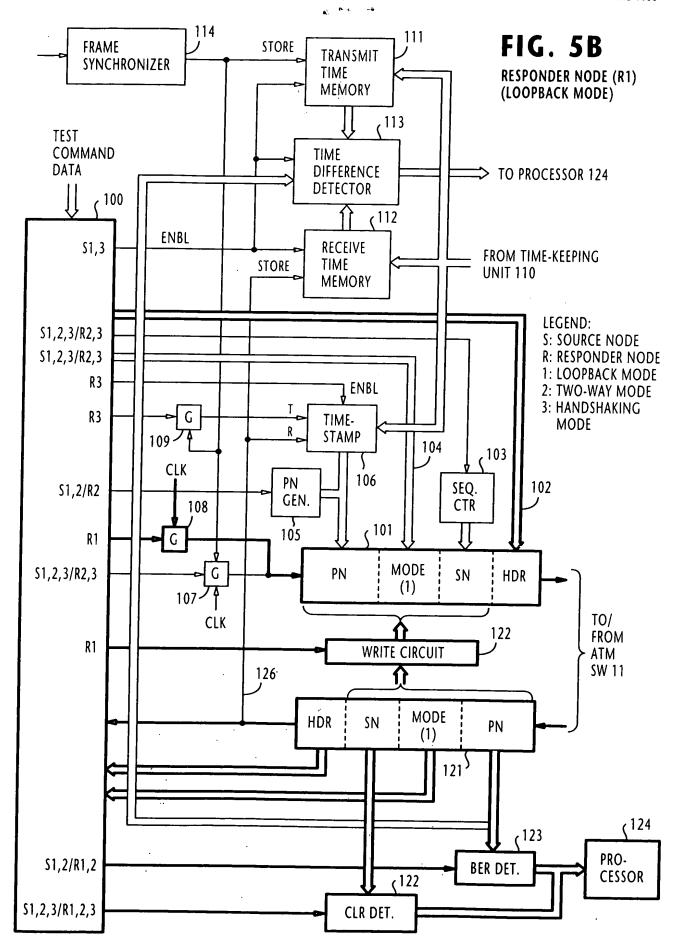
ATM CELL HEADER	
SN	
MODE 3	
DUMMY DATA	

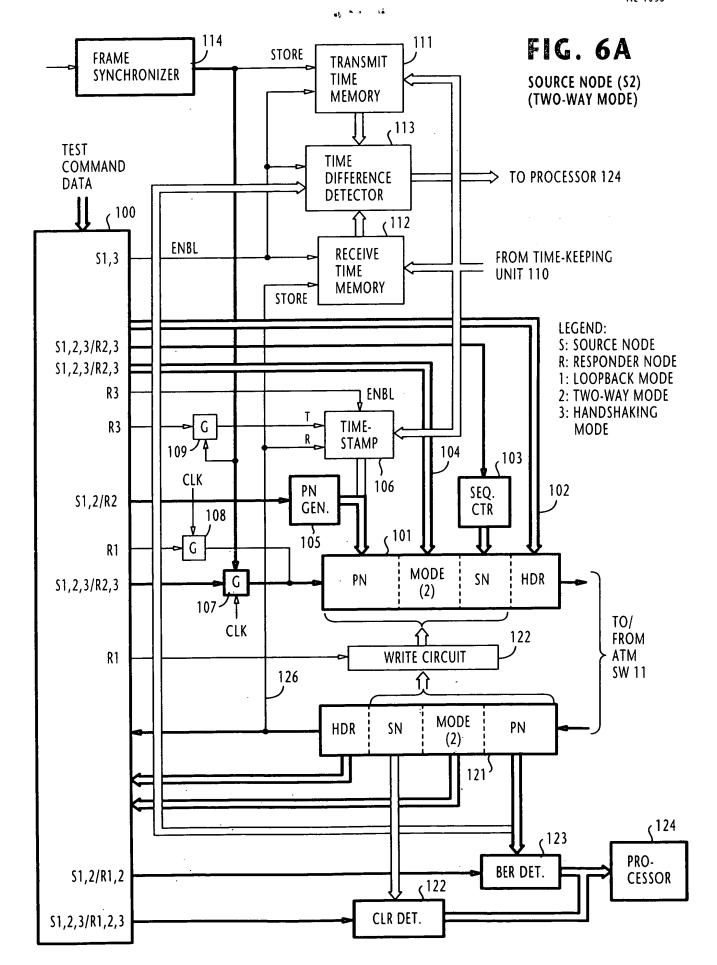
FIG. 3E
RESPONDER NODE
(HANDSHAKING MODE)

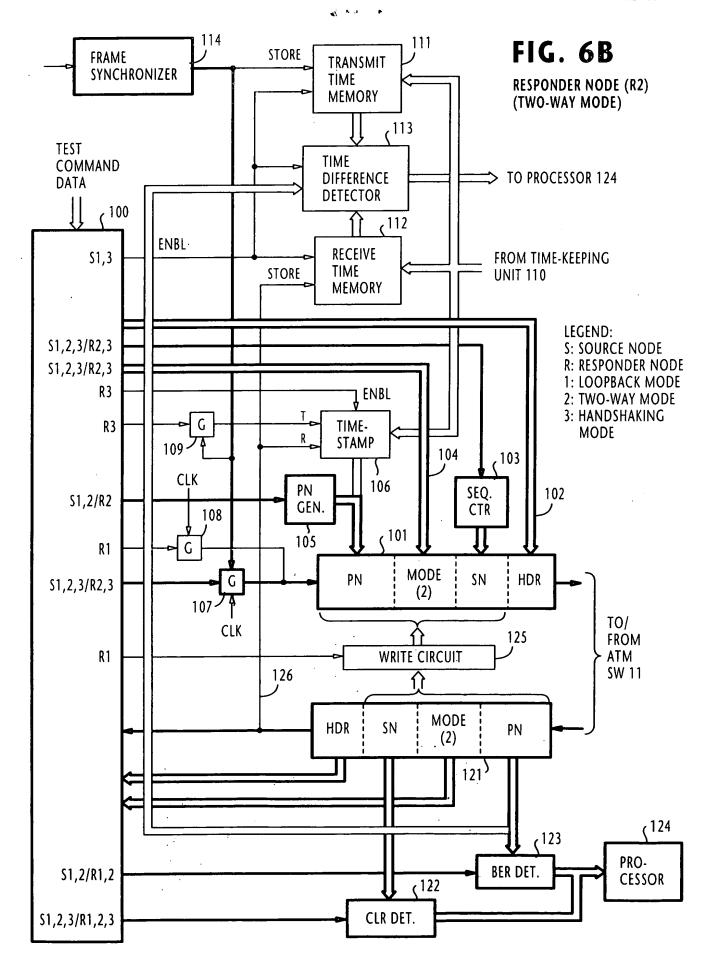
ATM CELL HEADER
SN
MODE 3
TIME-STAMPS

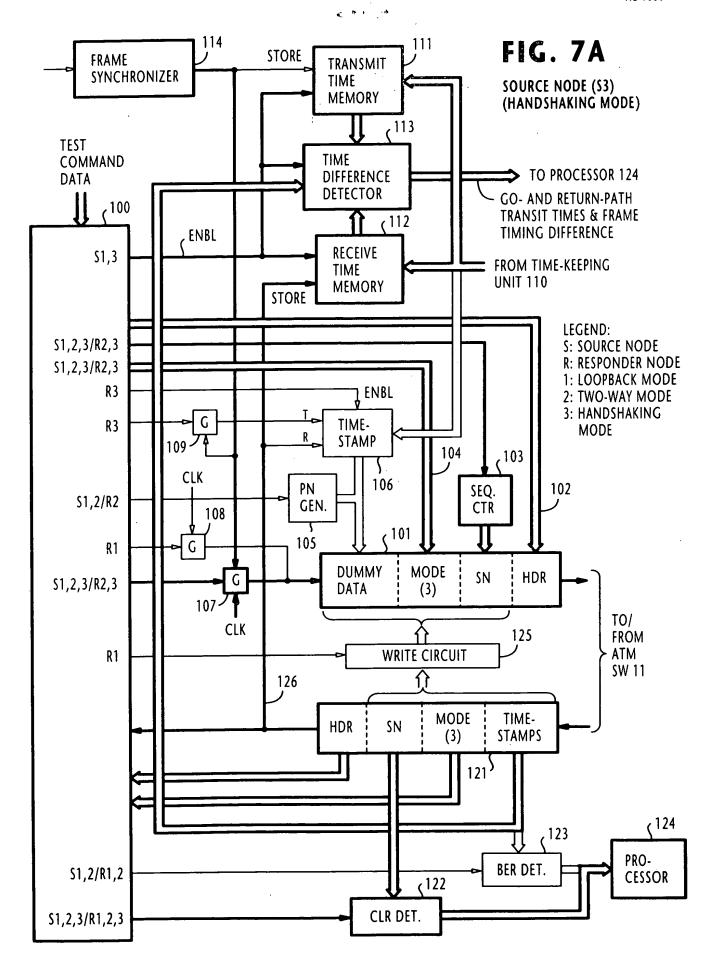












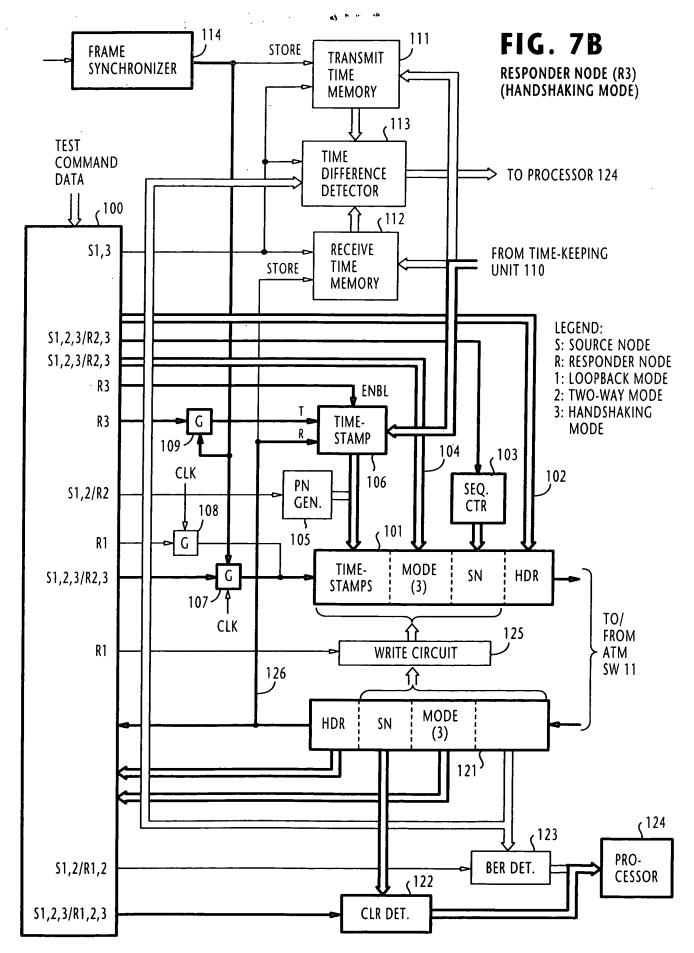


FIG. 8A LOOPBACK MODE

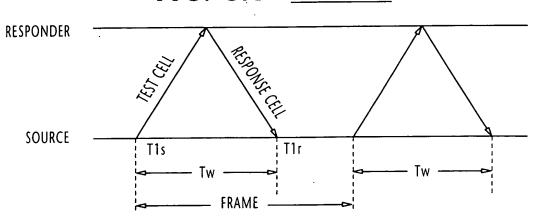


FIG. 8B TWO-WAY MODE

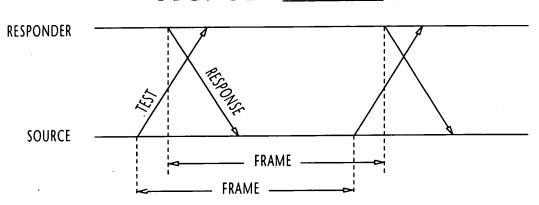
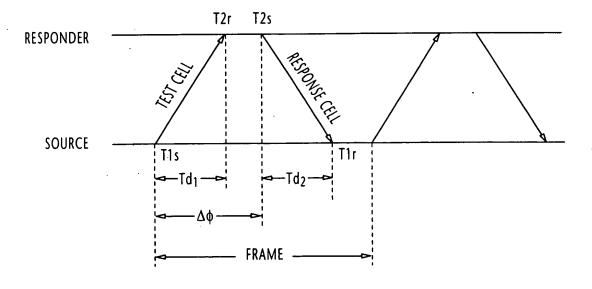


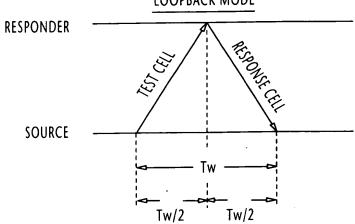
FIG. 8C HANDSHAKING MODE



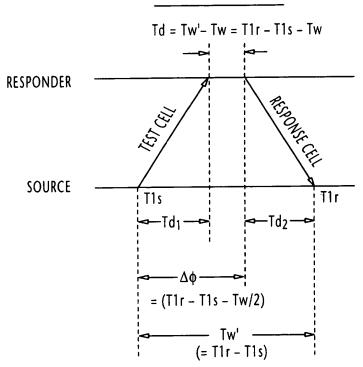
1 4 6 W

FIG. 9

## LOOPBACK MODE



## HANDSHAKING MODE



$$Td_1 = \Delta \phi - Td = \Delta \phi - (T1r - T1s - Tw)$$
  
 $Td_2 = Tw' - \Delta \phi = T1r - T1s - \Delta \phi$ 

<sub>Q1</sub>10 ti 1

FIG. 10

